

REMARKS

Claim 1 has been amended herein. Upon entry of this amendment, claims 1-5 will be pending in the above identified application.

Applicant acknowledges the allowance of claims 2 and 3.

Applicant appreciates the courtesies extended during the telephonic Interview conducted on January 28, 2004, between the with Examiner and Applicant's undersigned attorney.

Claims 1 and 4 – Section 112, first paragraph

Applicant respectfully requests reconsideration of the rejection of claims 1 and 4 under 35 U.S.C. § 112, first paragraph, as containing subject matter which was not described in the specification. The specification makes repeated disclosures of each of the plurality of mounted circuits being operated by setting a SEL signal for selectively turning a plurality of transistors on and off. For example, see page 6, lines 2-17; page 6, line 31 to page 8, line 21; and page 8, line 22 to page 9, line 7.

Because the specification properly describes the subject matter of claims 1 and 4, the Section 112, first paragraph, rejection is improper. Accordingly, Applicant respectfully requests that the rejection be withdrawn.

Claims 1-4 – Section 112, second paragraph

Applicant respectfully requests reconsideration of the rejection of claims 1-4 under 35 U.S.C. § 112, second paragraph. No support is provided for the rejection of claims 2 and 3. Thus, Applicant requests it be withdrawn.

The specification clearly shows that the subject matter particularly pointed to and distinctly claimed in claims 1 and 4 is included in what the Applicant regards as his invention. For example, see page 6, lines 2-17; page 6, line 31 to page 8, line 21; and page 8, line 22 to page 9, line 7.

Because the rejection of claims 1 and 4 under Section 112, second paragraph, is improper for at least the stated reasons, Applicant requests the rejection be withdrawn.

Claims 1 and 4 – Section 103(a)

Applicant respectfully requests reconsideration of the rejection of claims 1 and 4 under 35 U.S.C. § 103(a) as being unpatentable over U.S. Patent No. 6,446,249 (Wang) in view of U.S. Patent No. 5,566,107 (Gilliam). Claims 1 and 4 recite a selection circuit for selectively operating **only one of the plurality of mounted logic circuits**, wherein each of the plurality of mounted logic circuits is operated by setting an SEL signal for **selectively turning a plurality of transistors on and off** for each of the mounted logic circuits, to one of a high and a low level.

Wang discloses a circuit for an emulation system. Gilliam discloses a programmable circuit for enabling an associated circuit. Wang, Gilliam, or any combination thereof fails to disclose or suggest selectively operating only one of a plurality of mounted logic circuits, wherein each of the plurality of mounted logic circuits is operated by setting an SEL signal for **selectively turning a plurality of transistors on and off** for each of the mounted logic circuits, to one of a high and a low level.

After the Interview with the Examiner on January 28, 2004, the Examiner informed Applicant that he and his supervisor agreed with Applicant that Wang and Gilliam, individually or in any combination, do not support the rejection of claims 1 and 4. Examiner informed Applicant that an Interview Summary stating this agreement would be mailed to Applicant immediately.

Because Wang and Gilliam, individually and in any logical combination, do not show or suggest all of the elements of claims 1 and 4, the rejection of these claims is improper. Accordingly, Applicant requests the rejection be withdrawn.

Claim 5 – Section 103(a)

Applicant respectfully requests reconsideration of the rejection of claim 5 under 35 U.S.C. § 103(a) as being unpatentable over Wang in view of Gilliam. As amended, claim 5 recites a selection circuit for selectively operating only one of the plurality of mounted logic circuits wherein the selection circuit includes a transistor element connected in series with each logic circuit between the logic circuit and a power terminal, the transistor element selects a logic circuit to be operated on a basis of a signal input supplied through the input/output pad.

Wang discloses a circuit for an emulation system. Gilliam discloses a programmable circuit for enabling an associated circuit. Wang, Gilliam, or any combination

thereof fails to disclose or suggest a selection circuit for selectively operating only one of the plurality of mounted logic circuits wherein the selection circuit includes a transistor element connected in series with each logic circuit between the logic circuit and a power terminal, the transistor element selects a logic circuit to be operated on a basis of a signal input supplied through the input/output pad.

As previously mentioned, during an Interview with the Examiner on January 28, 2004, Examiner informed Applicant that he and his supervisor agreed that Wang and Gilliam, individually or in any combination, do support the rejection of claims 1 and 5.

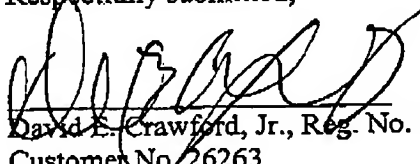
Because Wang and Gilliam, individually and in any logical combination, do not show or suggest all of the elements of claim 5, the rejection of it is improper. Accordingly, Applicant requests the rejection be withdrawn.

Conclusion

As it is believed that the application is in condition for allowance, a favorable action and Notice of Allowance are respectfully requested.

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Respectfully submitted,



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